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Revision Dates The chapters in this book, Cyclone Device Handbook, Volume 2, were revised on the following dates. Where chapters or groups of chapters are available separately, part numbers are listed.

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Hello, I'd like to have a confirmation of what I found on the Cyclone V Device Handbook regarding mixed voltage standards on a single bank. In my design I have multiple I/Os @ 1.8V and LVDS inputs on the same bank. Quartus doesn't issue any errors and the project compiles and fits successfully. I've...

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Corporation Volume 3  
1 Cyclone IV E

industrial devices I7  
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operating conditions for Cyclone IV devices. The values are based on experiments conducted with the device and

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Support To prevent core  
and I/O noise from  
coupling into the  
transceivers, the PMA  
block is isolated from  
the rest of the  
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supports the following device families: Arria II, Cyclone 10 LP, Cyclone IV, Cyclone V, MAX II, MAX V, and MAX 10 FPGA. To find software versions that support specific device families:

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Cyclone IV FPGA  
Device Family

Overview Revised:  
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Volume 1 Introduction  
Maximum user I/O pins  
158 182 315 315 475  
450 622 Notes to Table  
1-1 : (1) The EP2C15A  
is only available with  
the Fast On feature,  
which offers a faster  
POR time. This device  
is available in both  
commercial and  
industrial grade.

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## Cyclone Device Handbook, Volume 1

Cyclone devices are available in quad flat pack (QFP) and space-saving FineLine® BGA packages (see Tables 1-2 through 1-3).

Vertical migration means you can migrate a design from one device to another that has the same dedicated pins, JTAG pins, and power pins, and

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1. Overview for  
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V devices are ideal for  
small form factor  
applications that are  
cost- and. power-  
sensitive in the  
wireless, wireline,  
military, broadcast,  
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